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SEND TO: Examiner Nguyen, Kevin M.

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FAX NUMBER: 703.872.9314

TRANSMISSION DATE: 9 Feb 04 FILE REF: 09/818,081

FROM: Lin C. Hsu (46,315)

PAGE NUMBERS (including this sheet): 4 OPERATOR:

NOTES: Proposed Amendments to Claims for Examiner's Interview on 9 February 2004.

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PROPOSED AMENDMENTS

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FROM: **Lin Hsu**

TO: **Examiner Kevin Nguyen**

DATE: **9 February 2004**

RE: **Application No. 09/818,081**

Attorney Docket No. PALM-3628

Examiner Nguyen,

Here follows independent Claims 1, 13, and 19 with proposed amendments to overcome the Yokota prior art reference. In particular, emphasis is placed on fixed pixel border that surrounds the passive matrix. The claims are presented for discussion during the Examiner's Interview scheduled for February 9th at 3:00 p.m. EST.

1. (Suggested Amendments) A display unit comprising:

a passive matrix of independently controllable pixels comprising n rows and m columns of discrete pixels, said passive matrix operable to generate an image in response to electronic signals driven from row and column drivers coupled to said passive matrix, said image representative of information stored in a frame buffer memory; and

PAGE 2/4 * RCVD AT 2/9/2004 12:33:17 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/3 * DNIS:8729314 * CSID:4089389069 * DURATION (mm-ss):01-36

a fixed pixel border having a predetermined width, said pixel border surrounding said passive matrix and comprising a plurality of pixels which are uniformly controlled between an on and an off state by a common threshold signal.

13. (Suggested Amendments) A display unit comprising:

a passive matrix of independently controllable pixels comprising n rows and m columns of discrete pixels, said passive matrix being negative display mode and operable to generate an image in response to electronic signals driven from row and column drivers coupled to said passive matrix, said image representative of information stored in a frame buffer memory;

a fixed pixel border having a predetermined width, said pixel border surrounding said passive matrix and comprising a plurality of pixels which are uniformly controlled between an on and an off state by a common threshold signal; and

a contrast adjustment circuit for adjusting voltage levels supplied to said row and column drivers to adjust the contrast of said image of said passive matrix, wherein said contrast adjustment circuit is also operable to adjust said common threshold signal to match the contrast of said passive matrix.

19. (Suggested Amendments) A portable electronic device comprising:

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a processor coupled to a bus;

a memory unit coupled to said bus;

a user input device coupled to said bus; and

a display unit coupled to said bus and comprising:

a passive matrix of independently controllable pixels comprising n rows and m columns of discrete pixels, said passive matrix operable to generate an image in response to electronic signals driven from row and column drivers coupled to said passive matrix, said image representative of information stored in a frame buffer memory; and

a fixed pixel border having a predetermined width, said pixel border surrounding said passive matrix and comprising a plurality of pixels which are uniformly controlled between an on and an off state by a common threshold signal.

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